

Figure 1

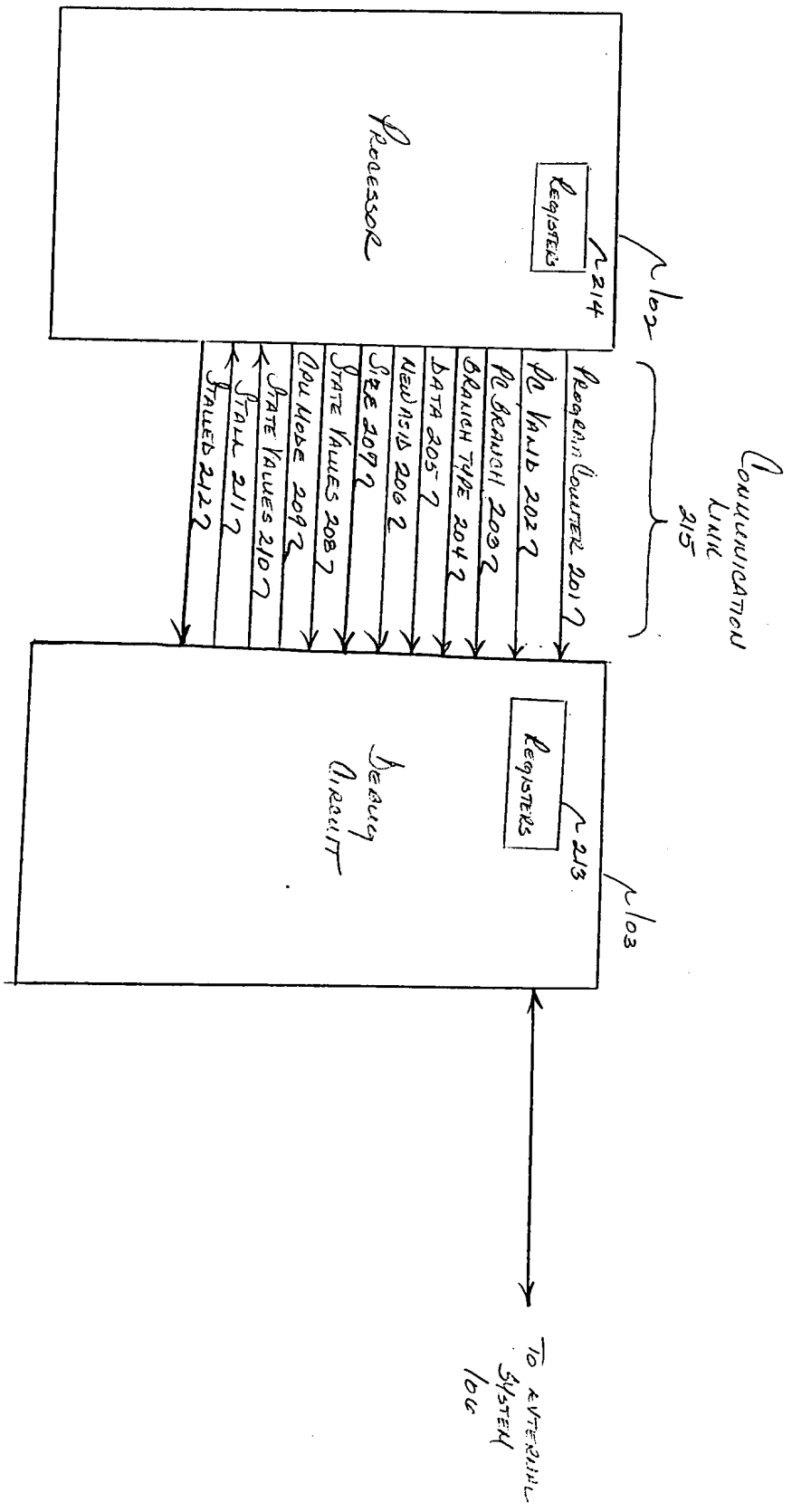


Figure 2

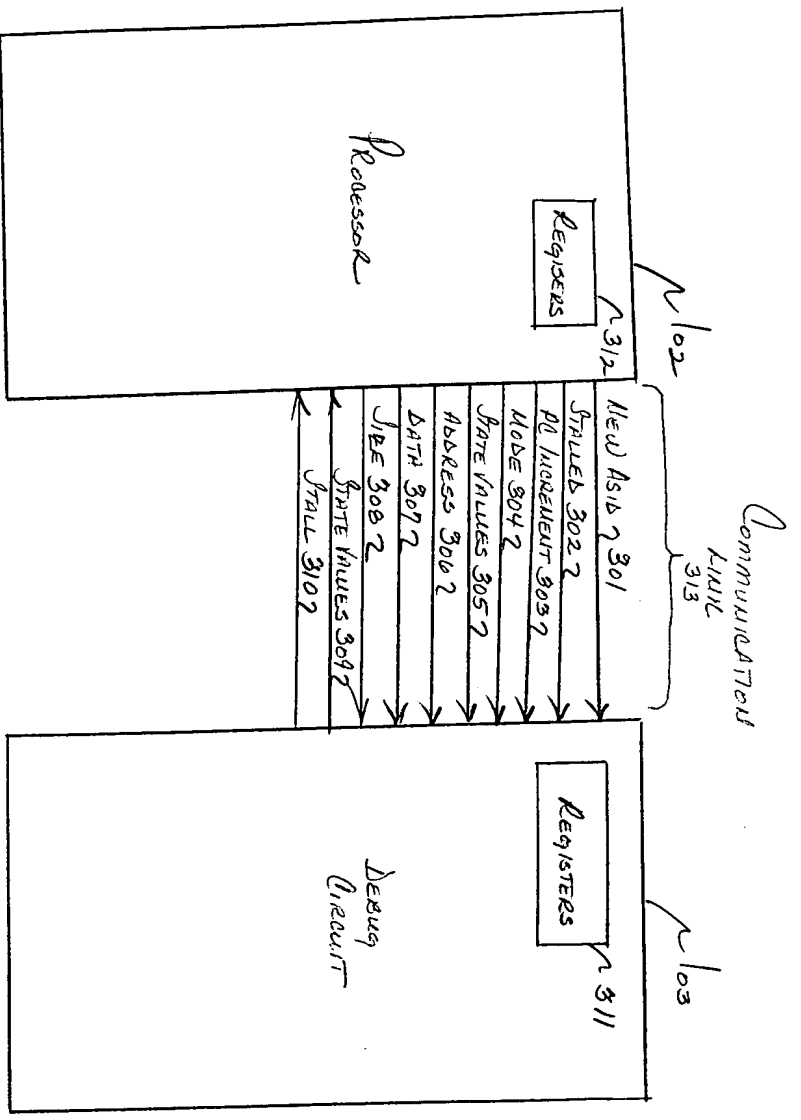


Figure 3

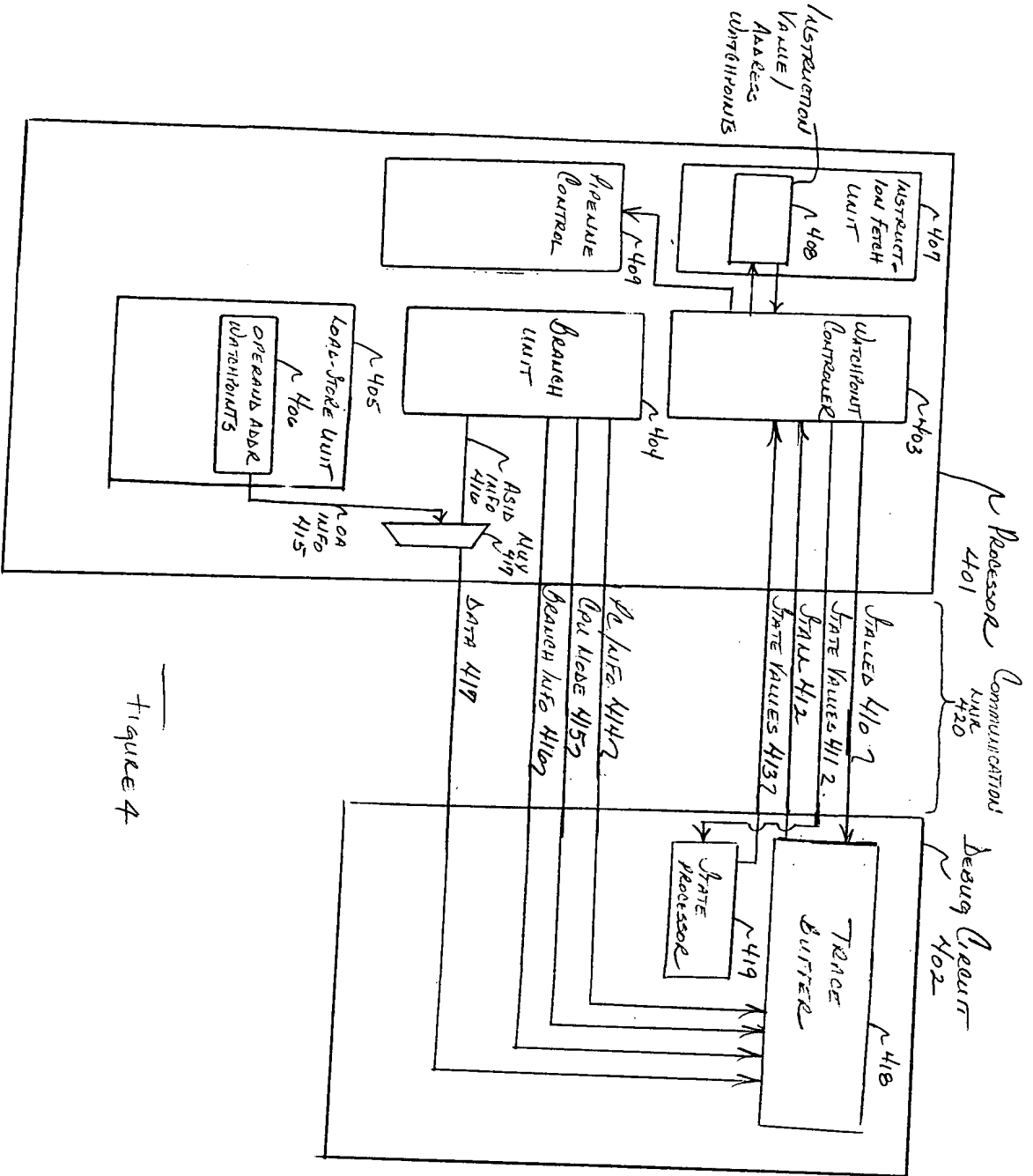


Figure 4

Processor
501

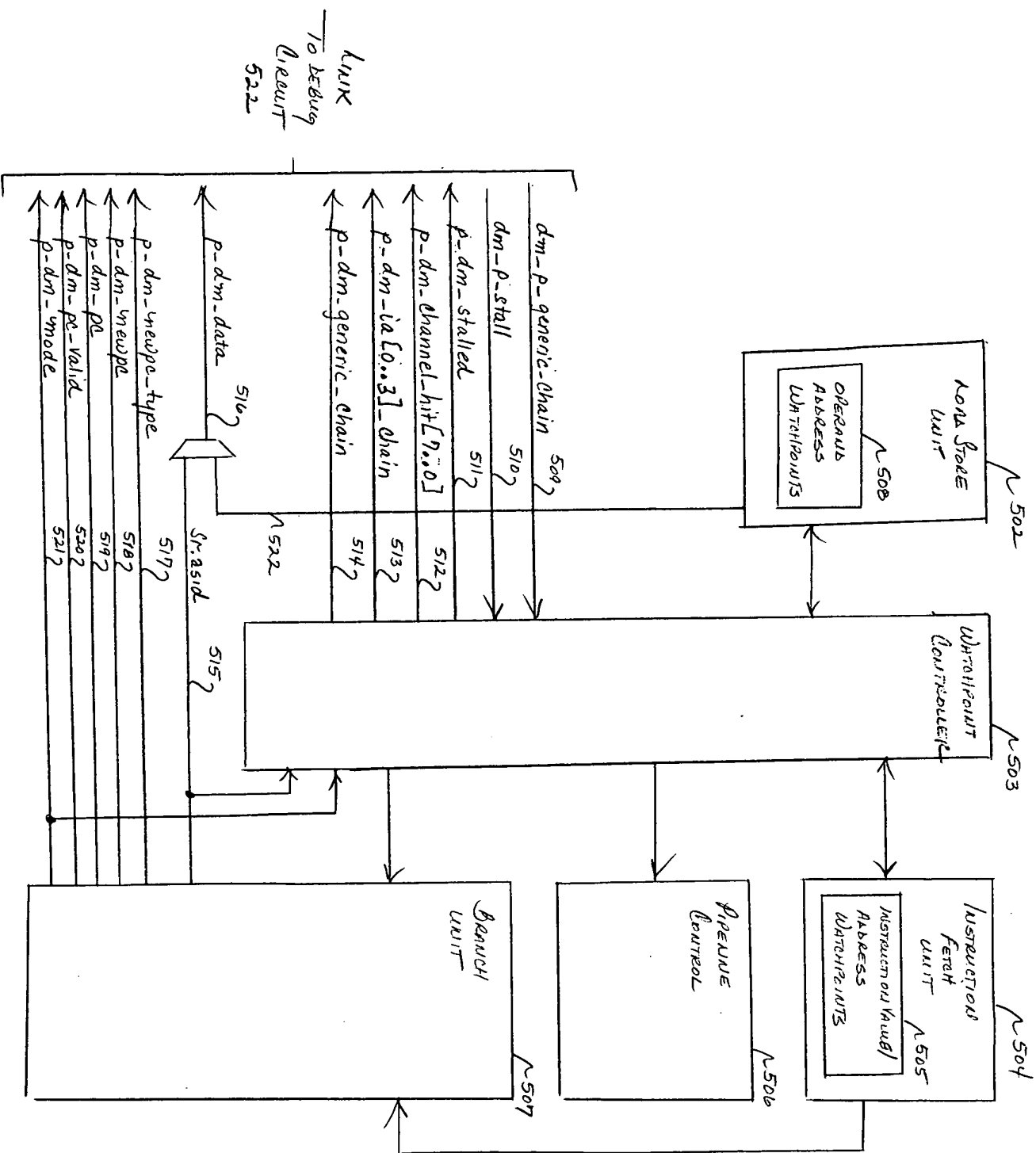


Figure 5

F
D
E1
E2
E3
W

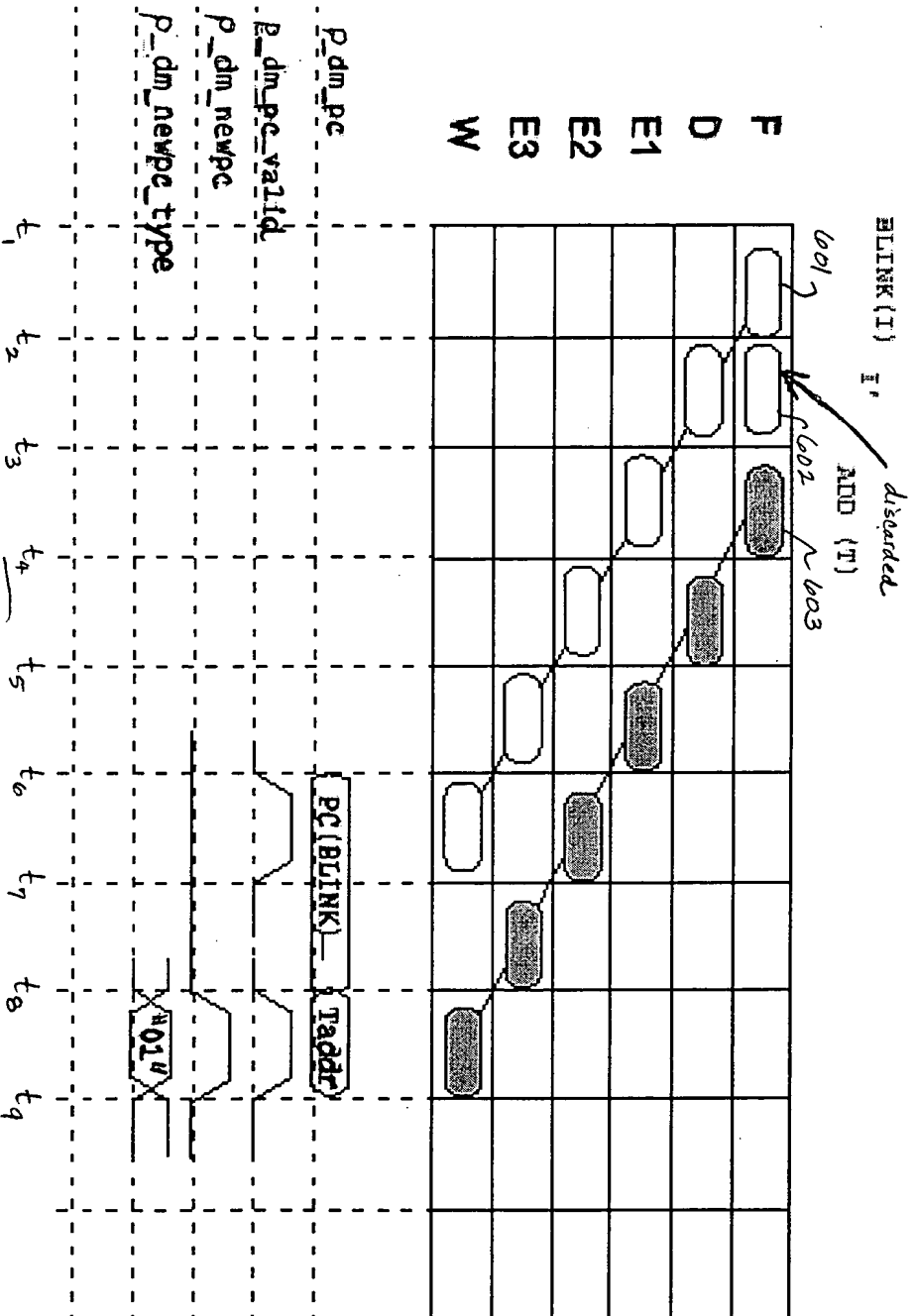


FIGURE 6